EE-390(a) - Lab on hardware-software digital systems codesign

Dear students,

The oral exam of the course will take place on Thursday, May 22nd, at 14:15 in ELG022. As discussed in class, you have to prepare a presentation showing how your system is designed to tackle the proposed problem and how your solution applies the contents covered during the course: problem partition between SW and HW (co-design), loop optimization, data caching and optimization of internal memories, memory coherence, device driver, etc. The presentation should have a duration of about 10 minutes. After it, we will have a five-minute round of questions and answers. The presentations must be uploaded before 14:15.

The presentations are private; therefore, each group has to wait for their turn outside of the classroom. You can find below an indicative time for your presentation:

TIME (orientative)	PERSON 1	PERSON 2
14:15	Overall presentation of submitted solutions	
14:30	Leonardo Vega	Jan Juraszek
14:45	Luca Caballero	Alexis Bertrand
15:00	Sarah Rerecich	Antoine Branca
15:15	Coline Mauprivez	
15:30	Eliot Abramo	Mathias Rainaldi
15:45	Antonin Vollard	Marco Romano
16:00	Adrian Karol	Ismail Hassoun
16:15	Thomas Tran	Dimitrios Renken
16:30	Gaëtan Jenni	Axel Juaneda

Before the round of presentations, we will show a summary of the solutions (anonymized) provided by all you and some insights gathered from them. We suggest that you join this part, but we will upload the presentation to Moodle after the exam.